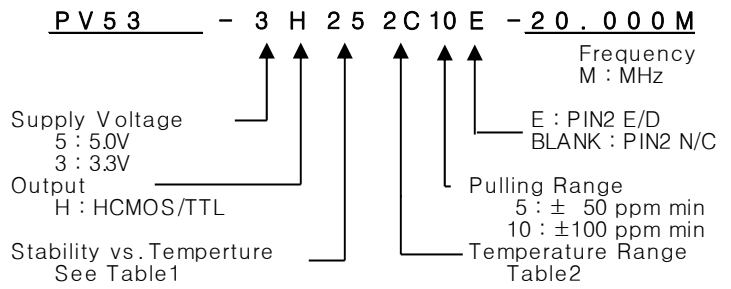


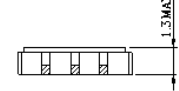
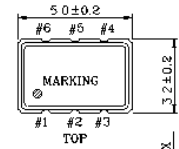
VCXO

PV53 Series
HCMOS/TTL
6PAD SMD PACKAGE

* PART NUMBERING GUIDE

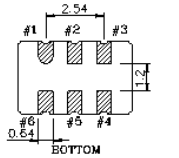


MECHANICAL DIMENSIONS

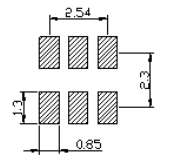


CONNECTION

- #1 V.C
- #2 E/D
- #3 GND
- #4 OUTPUT
- #5 N.C
- #6 Vec



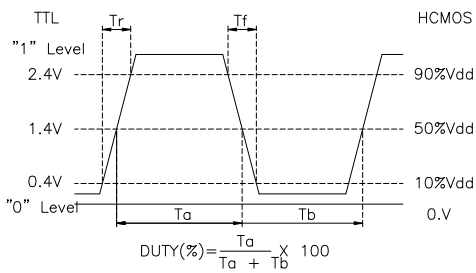
Recommended soldering pattern



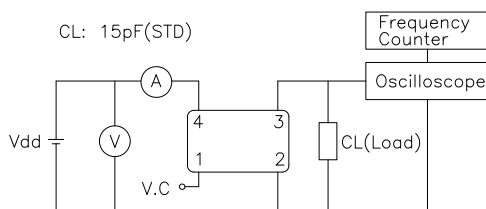
ELECTRICAL SPECIFICATION

Frequency range	1.5440MHz to 170.000MHz All combination of Frequency range Vs. Package type can not be available ,please contact factory	
Frequency Stability vs. Temperature vs. Aging	± 25 ppm to ±50ppm ±3.0 ppm max/ year	
Temperature Range Operating Storage	See Table 2 -55°C to 125°C	
Supply Voltage	3.3V ± 5% 5.0V ± 5%	
Input Current	30mA max.	
Output characteristics HCMOS / TTL	HCMOS	TTL
Logic "1"	90% Vdd min	2.4V min
Logic "1"	10% Vdd max	0.4V min
Load	15pF	10TTL
Duty Cycle	40/60	40/60
Rise & Fall	10nS max	10nS max
Pull Characteristics		
Pulling Range	±50ppm / ±100 ppm min	
Control Range	1.65V ± 1.5V (Vdd : 3.3V) 2.5V ± 2.5V (Vdd : 5.0V)	
Linearity	10% max.	
E/D function	PIN2 : 0.7Vdd min. or open , output Enabled PIN2 : 0.3Vdd max. , output Disabled	
RMS Jitter(12KHz~20MHz)	1.0ps max.	

OUTPUT WAVEFORM



TEST CIRCUIT



ENVIROMENTAL & MECHANICAL SPECIFICATION

Shock	MIL-STD-883C, Method 2002, Condition B
Vibration	MIL-STD-883C, Method 2007, Condition A
Solderability	MIL-STD-883C, Method 2003
Seal integrity	MIL-STD-883C, Method 1014, Condition C & A2
Marking	MIL-STD-202F, Method 215

TABLE1

Symbol	Stability
25	± 25ppm
30	± 30ppm
50	± 50ppm
XX	±XXppm

TABLE2

Symbol	Temp.	Symbol	Temp.
0	0°C	A	50°C
1	-10°C	B	60°C
2	-20°C	C	70°C
3	-30°C	D	75°C
4	-40°C	E	80°C
		F	85°C