

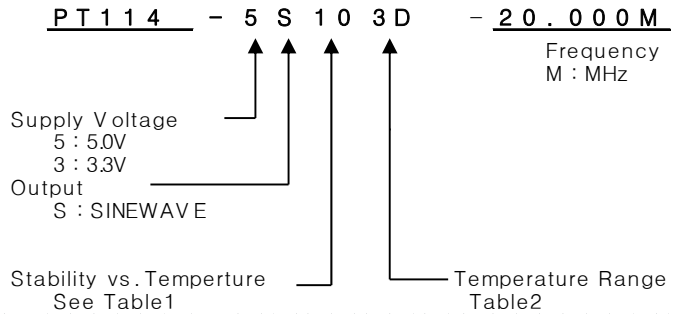
TCXO

PT114 Series

Sinewave

6PAD SMD PACKAGE

* PART NUMBERING GUIDE



MECHANICAL DIMENSIONS	ELECTRICAL SPECIFICATION			
<p style="text-align: center;">PIN CONNECTION</p> <p>#1 Vcc #2 N.C #3 GND #4 OUTPUT #5 GND #6 GND</p> <p style="text-align: center;">Recommended Soldering Pattern</p>	Frequency range	1.000MHz to 600.000MHz (All combinations for Frequency in the range and temp. stability can't be available, please contact factory.)		
	Frequency Stability vs. Temperature vs. Supply Voltage vs. Load vs. Aging	±0.5 ppm to ±5.0ppm ±0.1 / ±0.2 ppm max / Vdd ± 5% ±0.2 ppm max /15pF ±10% ±1.0 ppm max/ year		
	Temperature Range Operating Storage	See Table 2 -55°C to 125°C		
	Supply Voltage	3.3V ± 5% 5.0V ± 5%		
<h4>OUTPUT WAVEFORM</h4>	Input Current Sinewave	1.00MHz ~ 600.000MHz 10.0mA max ~ 50mA max		
	Output characteristics	Level 3.3V 5.0V Load Sinewave 0 dBm typ +5 dBm typ 50Ω		
	Phase Noise (typical) @20MHz	-80 dBc / Hz @ 10Hz -120 dBc / Hz @ 100Hz -135 dBc / Hz @ 1KHz -140 dBc / Hz @ 10KHz -145 dBc / Hz @100KHz		
	Frequency Adjustment	±3ppm min by internal trimmer		
<h4>TEST CIRCUIT</h4>	ENVIROMENTAL & MECHANICAL SPECIFICATION			
	Shock Vibration Solderability Seal integrity Marking	MIL-STD-883C, Method 2002, Condition B MIL-STD-883C, Method 2007, Condition A MIL-STD-883C, Method 2003 MIL-STD-883C, Method 1014, Condition C & A2 MIL-STD-202F, Method 215		
	TABLE1		TABLE2	
	Symbol	Stability	Symbol	Temp.
05	±0.5ppm	0	0°C	
10	±1.0ppm	A	50°C	
15	±1.5ppm	1	-10°C	
20	±2.0ppm	2	-20°C	
25	±2.5ppm	3	-30°C	
30	±3.0ppm	4	-40°C	
35	±3.5ppm			
50	±5.0ppm	F	85°C	