

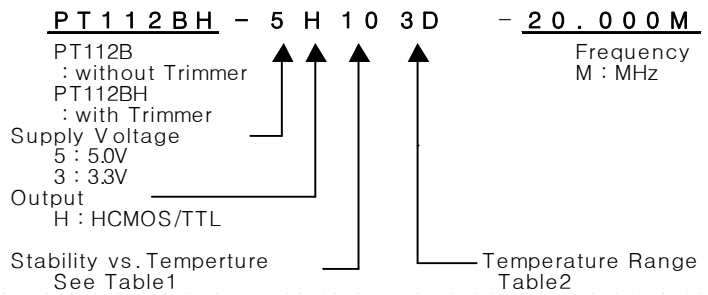
# TCXO

## PT112B Series

### HCMOS/TTL

### 6PAD SMD PACKAGE

#### \* PART NUMBERING GUIDE



MECHANICAL DIMENSIONS	ELECTRICAL SPECIFICATION																																		
<p>PIN CONNECTION</p> <p>#1 GND          #2 GND          #3 OUTPUT          #4 GND          #5 N.C          #6 Vcc</p> <p>Recommended Soldering Pattern</p>	<table border="1"> <tr> <td>Frequency range</td> <td>1.000KHz to 250.000MHz All combination of Frequency range Vs. Package type might not be available ,please contact factory.</td> </tr> <tr> <td>Frequency Stability vs. Temperature vs. Supply Voltage vs. Load vs. Aging</td> <td>±1.0 ppm to ±5.0ppm ±0.2 ppm max / Vdd ± 5% ±0.2 ppm max /15pF ±10% ±1.0 ppm max/ year</td> </tr> <tr> <td>Temperature Range Operating Storage</td> <td>See Table 2 -55°C to 125°C</td> </tr> <tr> <td>Supply Voltage</td> <td>3.3V ± 5% 5.0V ± 5%</td> </tr> <tr> <td>Input Current 3.3 V , 5V</td> <td>1.000KHz ~ 40.000MHz ~ 250.000MHz 15mA max ~ 30mA max ~ 100mA max</td> </tr> <tr> <td>Output characteristics</td> <td> <table border="1"> <thead> <tr> <th></th> <th>HCMOS</th> <th>TTL</th> </tr> </thead> <tbody> <tr> <td>Logic "1"</td> <td>90% Vdd min</td> <td>2.4V min</td> </tr> <tr> <td>Logic "1"</td> <td>10% Vdd max</td> <td>0.4V min</td> </tr> <tr> <td>Load</td> <td>15pF</td> <td>10TTL</td> </tr> <tr> <td>Duty Cycle</td> <td>40/60</td> <td>40/60</td> </tr> <tr> <td>Rise &amp; Fall</td> <td>10nS max</td> <td>10nS max</td> </tr> </tbody> </table> </td> </tr> <tr> <td>Phase Noise (typical) 20MHz offset</td> <td>           -80 dBc / Hz @ 10Hz            -120 dBc / Hz @ 100Hz            -135 dBc / Hz @ 1KHz            -140 dBc / Hz @ 10KHz            -145 dBc / Hz @100KHz         </td> </tr> <tr> <td>Frequency Adjustment</td> <td>±3ppm min by internal trimmer (Option/PT112BH only)</td> </tr> </table>	Frequency range	1.000KHz to 250.000MHz All combination of Frequency range Vs. Package type might not be available ,please contact factory.	Frequency Stability vs. Temperature vs. Supply Voltage vs. Load vs. Aging	±1.0 ppm to ±5.0ppm ±0.2 ppm max / Vdd ± 5% ±0.2 ppm max /15pF ±10% ±1.0 ppm max/ year	Temperature Range Operating Storage	See Table 2 -55°C to 125°C	Supply Voltage	3.3V ± 5% 5.0V ± 5%	Input Current 3.3 V , 5V	1.000KHz ~ 40.000MHz ~ 250.000MHz 15mA max ~ 30mA max ~ 100mA max	Output characteristics	<table border="1"> <thead> <tr> <th></th> <th>HCMOS</th> <th>TTL</th> </tr> </thead> <tbody> <tr> <td>Logic "1"</td> <td>90% Vdd min</td> <td>2.4V min</td> </tr> <tr> <td>Logic "1"</td> <td>10% Vdd max</td> <td>0.4V min</td> </tr> <tr> <td>Load</td> <td>15pF</td> <td>10TTL</td> </tr> <tr> <td>Duty Cycle</td> <td>40/60</td> <td>40/60</td> </tr> <tr> <td>Rise &amp; Fall</td> <td>10nS max</td> <td>10nS max</td> </tr> </tbody> </table>		HCMOS	TTL	Logic "1"	90% Vdd min	2.4V min	Logic "1"	10% Vdd max	0.4V min	Load	15pF	10TTL	Duty Cycle	40/60	40/60	Rise & Fall	10nS max	10nS max	Phase Noise (typical) 20MHz offset	-80 dBc / Hz @ 10Hz -120 dBc / Hz @ 100Hz -135 dBc / Hz @ 1KHz -140 dBc / Hz @ 10KHz -145 dBc / Hz @100KHz	Frequency Adjustment	±3ppm min by internal trimmer (Option/PT112BH only)
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