

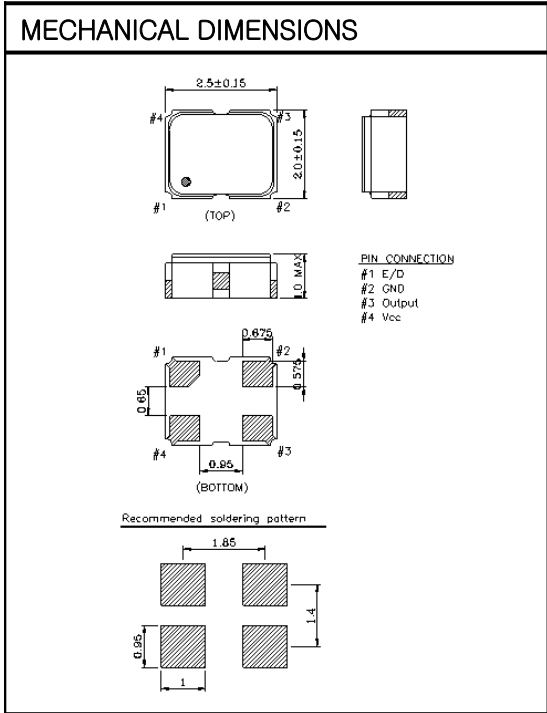
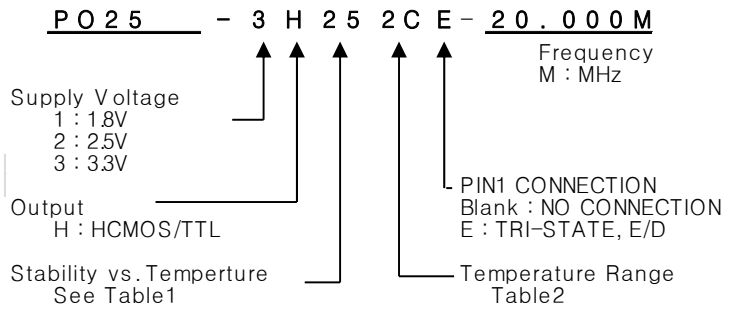
OSC

PO25 Series

HCMOS/TTL

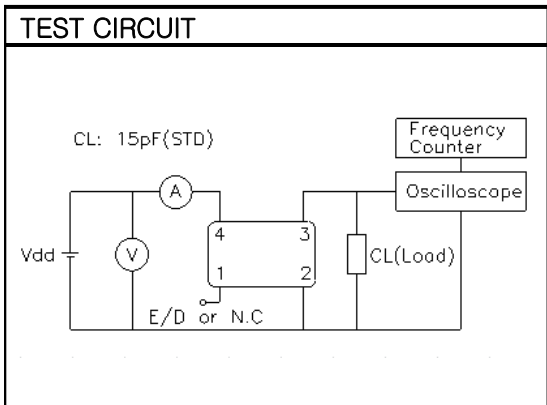
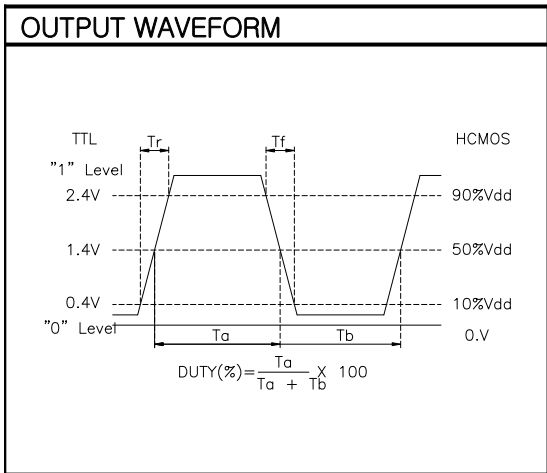
4PAD SMD PACKAGE

* PART NUMBERING GUIDE



ELECTRICAL SPECIFICATION

Frequency range	32.768KHz to 250.000MHz All combination of Frequency range Vs. Package type can not be available ,please contact factory																		
Frequency Stability vs. Temperature vs. Aging	±25 ppm to ±100ppm ±3.0 ppm max/ year																		
Temperature Range Operating Storage	See Table 2 (Wider operating temp. range available. Please contact) -55°C to 125°C																		
Supply Voltage	1.8V/ 2.5V/ 3.3V ± 10%																		
Input Current 3.3 V , 5V	32.768KHz fo ≤ 35.000MHz fo ≤ 50.000MHz fo > 50.000MHz 0.5mA 8mA 16mA 25mA																		
Output characteristics HCMOS / TTL	<table border="1"> <thead> <tr> <th></th> <th>HCMOS</th> <th>TTL</th> </tr> </thead> <tbody> <tr> <td>Logic "1" 90% Vdd min</td> <td>90% Vdd min</td> <td>2.4V min</td> </tr> <tr> <td>Logic "1" 10% Vdd max</td> <td>10% Vdd max</td> <td>0.4V min</td> </tr> <tr> <td>Load</td> <td>15pF</td> <td>10TTL</td> </tr> <tr> <td>Duty Cycle</td> <td>40/60</td> <td>40/60</td> </tr> <tr> <td>Rise & Fall</td> <td>10nS max</td> <td>10nS max</td> </tr> </tbody> </table>		HCMOS	TTL	Logic "1" 90% Vdd min	90% Vdd min	2.4V min	Logic "1" 10% Vdd max	10% Vdd max	0.4V min	Load	15pF	10TTL	Duty Cycle	40/60	40/60	Rise & Fall	10nS max	10nS max
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Phase Jitter (12KHz~20MHz)	1.0ps RMS max.																		



ENVIROMENTAL & MECHANICAL SPECIFICATION

Shock	MIL-STD-883C, Method 2002, Condition B
Vibration	MIL-STD-883C, Method 2007, Condition A
Solderability	MIL-STD-883C, Method 2003
Seal integrity	MIL-STD-883C, Method 1014, Condition C & A2
Marking	MIL-STD-202F, Method 215

TABLE1

Symbol	Stability
25	± 25ppm
30	± 30ppm
50	± 50ppm
100	±100ppm

TABLE2

Symbol	Temp.	Symbol	Temp.
0	0°C	A	50°C
1	-10°C	B	60°C
2	-20°C	C	70°C
3	-30°C	D	75°C
4	-40°C	E	80°C
		F	85°C