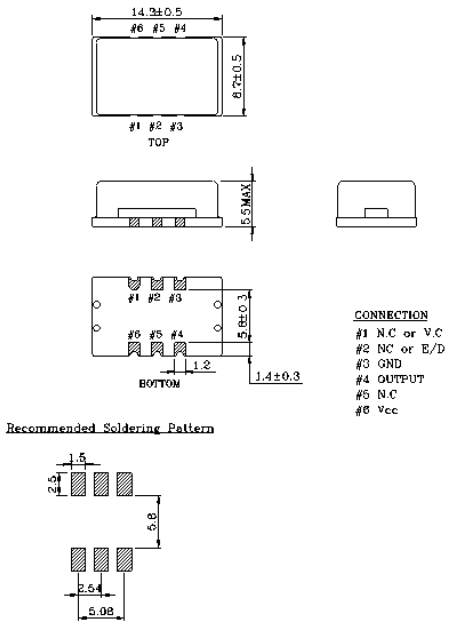
OSC
PO145B Series
HCMOS/TTL
6PAD SMD PACKAGE

* PART NUMBERING GUIDE

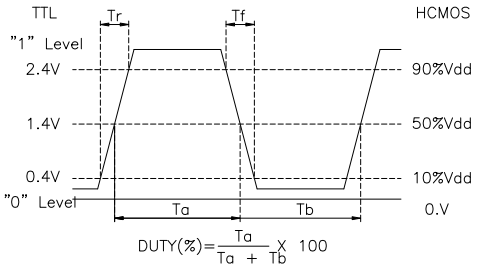
PO145B - 5 H 20 3D E - 50.000M

- Frequency: M : MHz
- Supply Voltage: 5 : 5.0V, 3 : 3.3V
- Output: H : HCMOS/TTL
- Stability vs. Temperture: See Table1
- Temperature Range: Table2
- PIN2 CONNECTION: Blank : NO CONNECTION, E : TRI-STATE, E/D

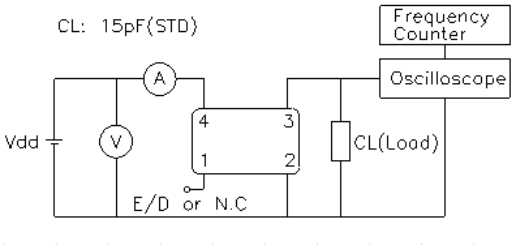
MECHANICAL DIMENSIONS



OUTPUT WAVEFORM



TEST CIRCUIT



ELECTRICAL SPECIFICATION

Frequency range	1.000KHz to 200.000MHz All combination of Frequency range Vs. Package type can not be available ,please contact factory
Frequency Stability vs. Temperature vs. Aging	 ±10 ppm to ±100ppm ±3.0 ppm max/ year
Temperature Range Operating Storage	 See Table 2 -55℃ to 105℃
Supply Voltage	3.3V ± 5% 5.0V ± 5%
Input Current	1.000KHz ~ 200.000MHz 15mA max ~ 60mA max
Output characteristics HCMOS / TTL	HCMOS / TTL Logic "1" 90% Vdd min / 2.4V min Logic "1" 10% Vdd max / 0.4V min Load 15pF / 10TTL Duty Cycle 40/60 / 40/60 Rise & Fall 10nS max / 10nS max
Pin 2 Tri-State Input Voltage	 No Connection Enable Output Vh ≥ 2.0 Vdc Enable Output Vl ≤ 0.8 Vdc Disable Output
Phase noise typ. @50MHz	-96dBc/Hz @10Hz -135dBc/Hz @100Hz -155dBc/Hz @1KHz -165dBc/Hz @10KHz -168dBc/Hz @100KHz -170dBc/Hz@1MHz

ENVIROMENTAL & MECHANICAL SPECIFICATION

Shock	MIL-STD-883C, Method 2002, Condition B
Vibration	MIL-STD-883C, Method 2007, Condition A
Solderability	MIL-STD-883C, Method 2003
Seal integrity	MIL-STD-883C, Method 1014, Condition C & A2
Marking	MIL-STD-202F, Method 215

TABLE1

Symbol	Stability
10	± 10ppm
15	± 15ppm
20	± 20ppm
30	± 30ppm
50	± 50ppm
100	±100ppm

TABLE2

Symbol	Temp.	Symbol	Temp.
0	0℃	A	50℃
1	-10℃	B	60℃
2	-20℃	C	70℃
3	-30℃	D	75℃
4	-40℃	E	80℃
		F	85℃

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