

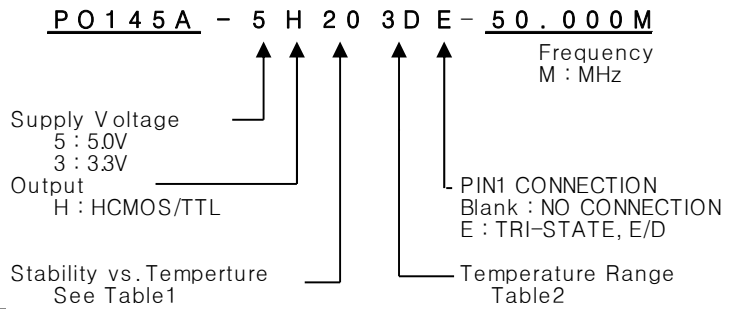
OSC

PO145A Series

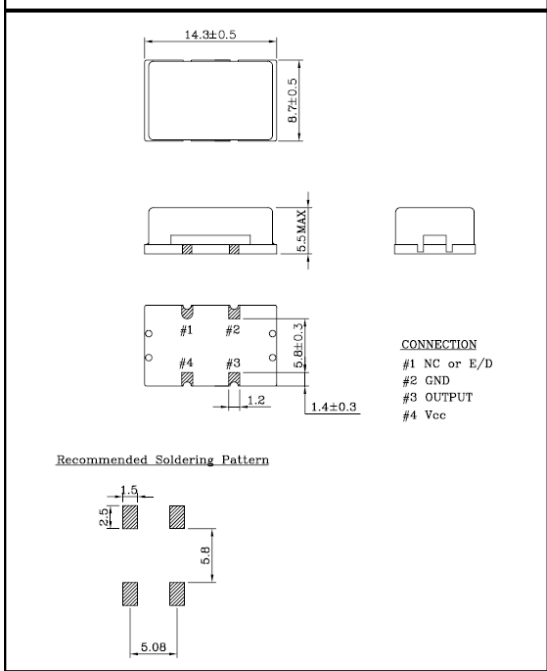
HCMOS/TTL

4PAD SMD PACKAGE

* PART NUMBERING GUIDE



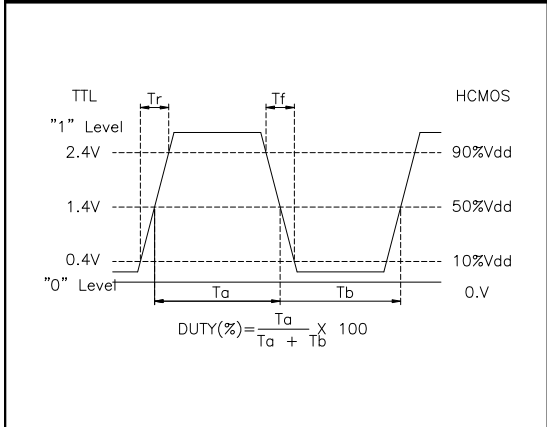
MECHANICAL DIMENSIONS



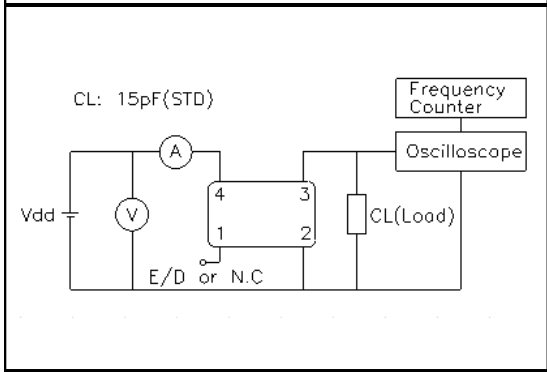
ELECTRICAL SPECIFICATION

Frequency range	1.000KHz to 200.000MHz All combination of Frequency range Vs. Package type can not be available ,please contact factory	
Frequency Stability vs. Temperature vs. Aging	±10 ppm to ±100ppm ±3.0 ppm max/ year	
Temperature Range Operating Storage	See Table 2 -55°C to 105°C	
Supply Voltage	3.3V ± 5% 5.0V ± 5%	
Input Current	1.000KHz ~ 200.000MHz 10mA max ~ 60mA max	
Output characteristics HCMOS / TTL	HCMOS	TTL
Logic "1"	90% Vdd min	2.4V min
Logic "1"	10% Vdd max	0.4V min
Load	15pF	10TTL
Duty Cycle	40/60	40/60
Rise & Fall	10nS max	10nS max
Pin 1 Tri-State Input Voltage	No Connection Vh ≥ 2.0 Vdc Vl ≤ 0.8 Vdc	Enable Output Enable Output Disable Output
Phase noise typ. @50MHz	-96dBc/Hz @10Hz -135dBc/Hz @100Hz -155dBc/Hz @1KHz -165dBc/Hz @10KHz -168dBc/Hz @100KHz -170dBc/Hz@1MHz	

OUTPUT WAVEFORM



TEST CIRCUIT



ENVIROMENTAL & MECHANICAL SPECIFICATION

Shock	MIL-STD-883C, Method 2002, Condition B
Vibration	MIL-STD-883C, Method 2007, Condition A
Solderability	MIL-STD-883C, Method 2003
Seal integrity	MIL-STD-883C, Method 1014, Condition C & A2
Marking	MIL-STD-202F, Method 215

Symbol	Stability
10	± 10ppm
15	± 15ppm
20	± 20ppm
30	± 30ppm
50	± 50ppm
100	±100ppm

Symbol	Temp.	Symbol	Temp.
0	0°C	A	50°C
1	-10°C	B	60°C
2	-20°C	C	70°C
3	-30°C	D	75°C
4	-40°C	E	80°C
		F	85°C