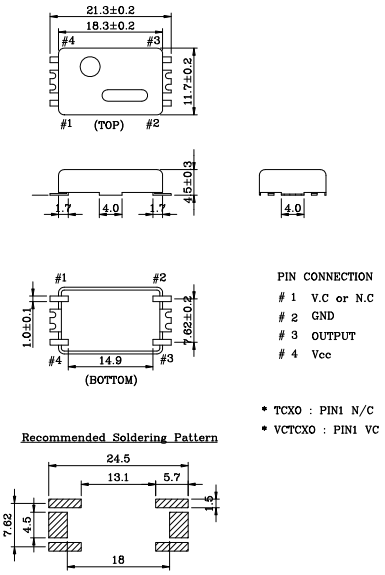
VCTCXO
PC184B Series
Sinewave
4PAD SMD PACKAGE

* PART NUMBERING GUIDE

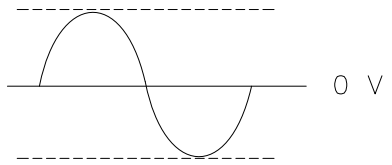
P C 1 8 4 B - 5 S 1 0 3 D 5 - 1 0 . 0 0 0 M

- Supply Voltage: 5 : 5.0V, 3 : 3.3V
- Output: S : Sinewave
- Stability vs. Temperture: See Table1
- Temperature Range: Table2
- Pulling Range: 5 : ± 5 ppm min, 10 : ±10 ppm min
- Frequency: M : MHz

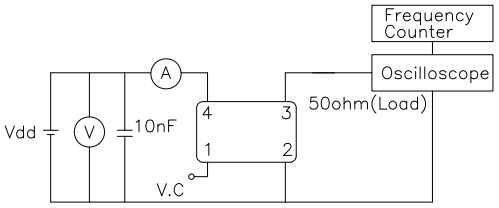
MECHANICAL DIMENSIONS



OUTPUT WAVEFORM



TEST CIRCUIT



ELECTRICAL SPECIFICATION

Frequency range	1.000MHz to 600.000MHz (All combinations for Frequency in the range and temp. stability can't be available, please contact factory.)
Frequency Stability vs. Temperature vs. Supply Voltage vs. Load vs. Aging	 ±0.5 ppm to ±5.0ppm ±0.1 / ±0.2 ppm max / Vdd ± 5% ±0.2 ppm max /15pF ±10% ±1.0 ppm max/ year
Temperature Range Operating Storage	 See Table 2 -55℃ to 125℃
Supply Voltage	3.3V ± 5% 5.0V ± 5%
Input Current Sinewave	 1.00MHz ~ 600.000MHz 10.0mA max ~ 50mA max
Output characteristics	Sinewave Level 3.3V 0 dBm typ 5.0V +5 dBm typ Load 50Ω
Phase Noise (typical) 20MHz offset	-80 dBc / Hz @ 10Hz -120 dBc / Hz @ 100Hz -135 dBc / Hz @ 1KHz -140 dBc / Hz @ 10KHz -145 dBc / Hz @100KHz
Frequency Adjustment	±3ppm min by internal trimmer
Voltage Control Characteristics	
Output Pulling Range (△F/△V) Control Voltage Range	±5.0ppm or ±10ppm min (△F/△V >±20ppm is available, please contact us) 1.65V ± 1.5V (Vdd : 3.3V), 2.5V ± 2.0V (Vdd : 5.0V)

ENVIROMENTAL & MECHANICAL SPECIFICATION

Shock	MIL-STD-883C, Method 2002, Condition B
Vibration	MIL-STD-883C, Method 2007, Condition A
Solderability	MIL-STD-883C, Method 2003
Seal integrity	MIL-STD-883C, Method 1014, Condition C & A2
Marking	MIL-STD-202F, Method 215

TABLE1

Symbol	Stability
05	±0.5ppm
10	±1.0ppm
15	±1.5ppm
20	±2.0ppm
25	±2.5ppm
30	±3.0ppm
35	±3.5ppm
50	±5.0ppm

TABLE2

Symbol	Temp.	Symbol	Temp.
0	0℃	A	50℃
1	-10℃	B	60℃
2	-20℃	C	70℃
3	-30℃	D	75℃
4	-40℃	E	80℃
		F	85℃

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