

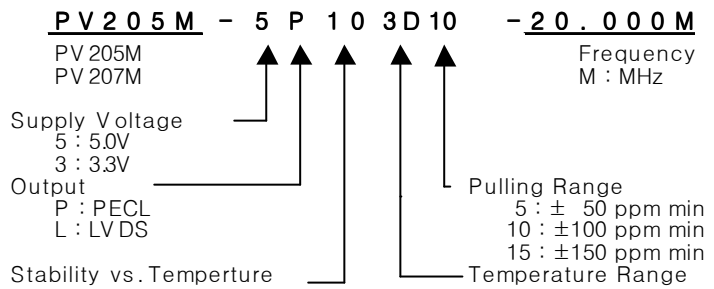
# VCXO

PV205M/PV207M Series

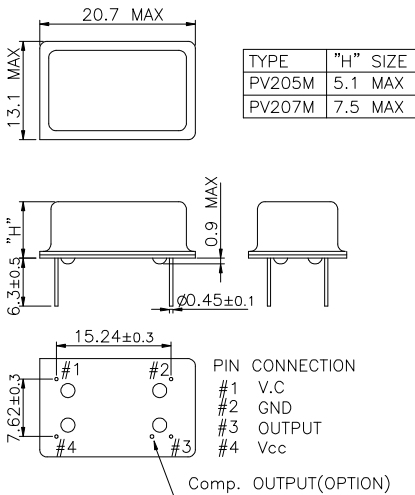
PECL/LVDS

14PIN DIP PACKAGE

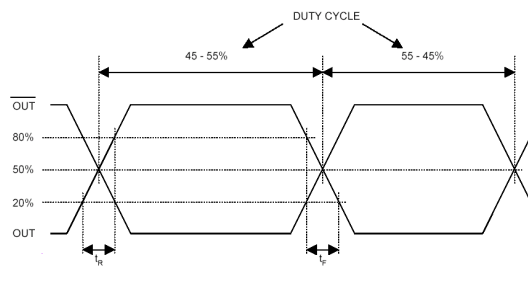
## \* PART NUMBERING GUIDE



### MECHANICAL DIMENSIONS

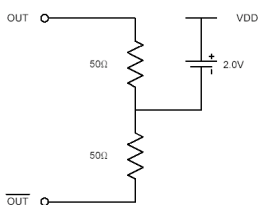


### OUTPUT WAVEFORM

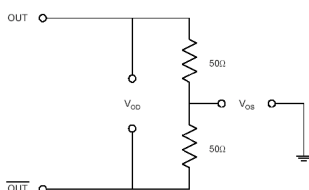


### TEST CIRCUIT

PECL Levels Test Circuit



LVDS Levels Test Circuit



### ELECTRICAL SPECIFICATION

Frequency range	0.75MHz to 800.000MHz All combination of Frequency range Vs. Package type might not be available ,please contact factory	
Frequency Stability vs. Temperature vs. Aging	$\pm 10$ ppm to $\pm 50$ ppm $\pm 3.0$ ppm max/ year	
Temperature Range Operating Storage	See Table 2 -55 $^{\circ}$ C to 105 $^{\circ}$ C	
Supply Voltage	3.3V $\pm 5\%$ 5.0V $\pm 5\%$	
Input Current 3.3 V , 5V	24.000MHz ~ 25mA max	800.000MHz ~ 100mA max
Output characteristics	pecl	lvds
Voh Logic "1"	Vdd-1.025v min.	1.43v typ.
Vol Logic "0"	Vdd-1.620v max.	1.10v typ.
Rise Time Tr	1.0 nsec max.	1.0 nsec max.
Fall Time Tf	1.0 nsec min.	1.0 nsec min.
Duty Cycle	50//50 $\pm 5\%$	50//50 $\pm 5\%$
Differential Output	Vod(Lvds)	330mV typ.
Offset Voltage	Vos(Lvds)	1.2V typ
Pull Characteristics		
Pulling Range	$\pm 50$ ppm / $\pm 100$ / $\pm 150$ ppm min Wide pulling range : contact company	
Control Range	1.65V $\pm 1.5$ V ( Vdd : 3.3V ) 2.5V $\pm 2.5$ V ( Vdd : 5.0V )	
JITTER (RMS)	Phase Jitter (12KHz ~ 20MHz)	1.0 psec MAX

### ENVIROMENTAL & MECHANICAL SPECIFICATION

Shock	MIL-STD-883C, Method 2002, Condition B
Vibration	MIL-STD-883C, Method 2007, Condition A
Solderability	MIL-STD-883C, Method 2003
Seal integrity	MIL-STD-883C, Method 1014, Condition C & A2
Marking	MIL-STD-202F, Method 215

#### TABLE1

Symbol	Stability
10	$\pm 10$ ppm
15	$\pm 15$ ppm
20	$\pm 20$ ppm
30	$\pm 30$ ppm
50	$\pm 50$ ppm
100	$\pm 100$ ppm

#### TABLE2

Symbol	Temp.	Symbol	Temp.
0	0 $^{\circ}$ C	A	50 $^{\circ}$ C
1	-10 $^{\circ}$ C	B	60 $^{\circ}$ C
2	-20 $^{\circ}$ C	C	70 $^{\circ}$ C
3	-30 $^{\circ}$ C	D	75 $^{\circ}$ C
4	-40 $^{\circ}$ C	E	80 $^{\circ}$ C
		F	85 $^{\circ}$ C