

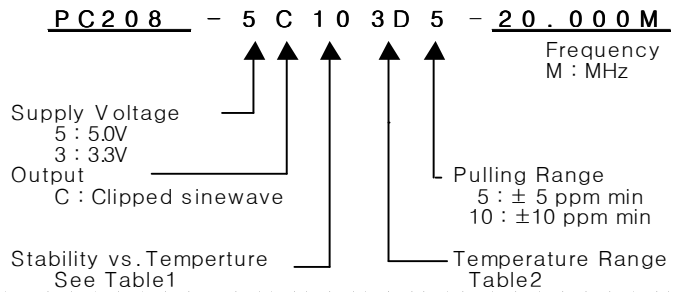
VCTCXO

PC208 Series

Clipped sinewave

5PIN DIP PACKAGE

* PART NUMBERING GUIDE



MECHANICAL DIMENSIONS	ELECTRICAL SPECIFICATION																																															
<p>PIN CONNECTION #1 V.C #2 Output #3 Supply Voltage #4 GND #5 GND</p>	Frequency range	1.8432MHz to 190.000MHz																																														
	Frequency Stability vs. Temperature vs. Supply Voltage vs. Load vs. Aging	± 0.5 ppm to ± 5.0 ppm $\pm 0.1 / \pm 0.2$ ppm max / $V_{dd} \pm 5\%$ ± 0.2 ppm max / $15\text{pF} \pm 10\%$ ± 1.0 ppm max/ year																																														
	Temperature Range Operating Storage	See Table 2 -55°C to 125°C																																														
	Supply Voltage	$3.3\text{V} \pm 5\%$ $5.0\text{V} \pm 5\%$																																														
	Input Current Clipped sinewave	6.00MHz ~ 190.000MHz 2.0mA max ~ 30mA max																																														
	Output characteristics	Level 3.3V 0.8Vp-p min 5.0V 1.0Vp-p min Load 10k Ω //10pF																																														
	Phase Noise (typical) 20MHz offset	-80 dBc / Hz @ 10Hz -120 dBc / Hz @ 100Hz -135 dBc / Hz @ 1KHz -140 dBc / Hz @ 10KHz -145 dBc / Hz @ 100KHz																																														
	Frequency Adjustment	± 3 ppm min by internal trimmer																																														
	Voltage Control Characteristics																																															
	Output Pulling Range ($\Delta F / \Delta V$)	± 5.0 ppm or ± 10 ppm min ($\Delta F / \Delta V > \pm 20$ ppm is available, please contact us)																																														
	Control Voltage Range	$1.65\text{V} \pm 1.5\text{V}$ ($V_{dd} : 3.3\text{V}$), $2.5\text{V} \pm 2.0\text{V}$ ($V_{dd} : 5.0\text{V}$)																																														
OUTPUT WAVEFORM 																																																
	ENVIRONMENTAL & MECHANICAL SPECIFICATION																																															
	Shock Vibration Solderability Seal integrity Marking	MIL-STD-883C, Method 2002, Condition B MIL-STD-883C, Method 2007, Condition A MIL-STD-883C, Method 2003 MIL-STD-883C, Method 1014, Condition C & A2 MIL-STD-202F, Method 215																																														
TEST CIRCUIT 																																																
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