

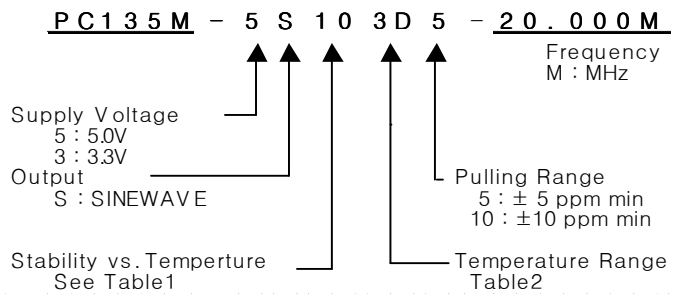
VCTCXO

PC135M Series

Sinewave

8PIN DIP PACKAGE

* PART NUMBERING GUIDE



MECHANICAL DIMENSIONS	ELECTRICAL SPECIFICATION			
	Frequency range	10.000MHz to 50.000MHz		
	Frequency Stability vs. Temperature vs. Supply Voltage vs. Load vs. Aging	± 0.5 ppm to ± 5.0 ppm $\pm 0.1 / \pm 0.2$ ppm max / $V_{dd} \pm 5\%$ ± 0.2 ppm max / $15\text{pF} \pm 10\%$ ± 1.0 ppm max/ year		
	Temperature Range Operating Storage	See Table 2 -55°C to 125°C		
	Supply Voltage	$3.3\text{V} \pm 5\%$ $5.0\text{V} \pm 5\%$		
	Input Current Sinewave	10.00MHz 12.0mA max	\sim \sim	50.000MHz 30mA max
	Output characteristics	Level 3.3V 5.0V Load	Sinewave 0 dBm typ 10 dBm typ 50Ω	
	Phase Noise (typical) 20MHz offset	$-80\text{ dBc / Hz @ } 10\text{Hz}$ $-120\text{ dBc / Hz @ } 100\text{Hz}$ $-135\text{ dBc / Hz @ } 1\text{KHz}$ $-140\text{ dBc / Hz @ } 10\text{KHz}$ $-145\text{ dBc / Hz @ } 100\text{KHz}$		
	Frequency Adjustment	$\pm 3\text{ppm min}$ by internal trimmer		
	Voltage Control Characteristics			
	Output Pulling Range ($\Delta F / \Delta V$) Control Voltage Range	$\pm 5.0\text{ppm}$ or $\pm 10\text{ppm min}$ ($\Delta F / \Delta V > \pm 20\text{ppm}$ is available, please contact us) $1.65\text{V} \pm 1.5\text{V}$ ($V_{dd} : 3.3\text{V}$), $2.5\text{V} \pm 2.0\text{V}$ ($V_{dd} : 5.0\text{V}$)		
ENVIROMENTAL & MECHANICAL SPECIFICATION				
Shock	MIL-STD-883C, Method 2002, Condition B			
Vibration	MIL-STD-883C, Method 2007, Condition A			
Solderability	MIL-STD-883C, Method 2003			
Seal integrity	MIL-STD-883C, Method 1014, Condition C & A2			
Marking	MIL-STD-202F, Method 215			
TABLE1		TABLE2		
Symbol	Stability	Symbol	Temp.	
05	$\pm 0.5\text{ppm}$	0	0°C	
10	$\pm 1.0\text{ppm}$	A	50°C	
15	$\pm 1.5\text{ppm}$	1	-10°C	
20	$\pm 2.0\text{ppm}$	2	-20°C	
25	$\pm 2.5\text{ppm}$	3	-30°C	
30	$\pm 3.0\text{ppm}$	4	-40°C	
35	$\pm 3.5\text{ppm}$			
50	$\pm 5.0\text{ppm}$	F	85°C	
TEST CIRCUIT				