

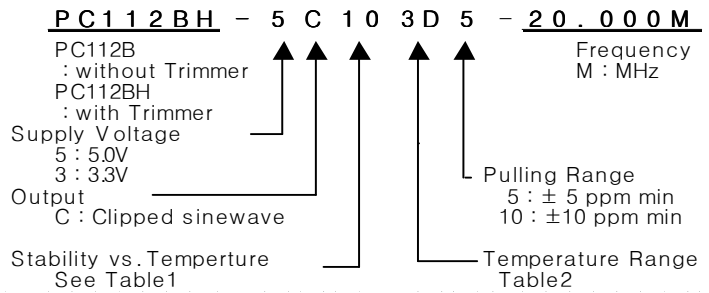
VCTCXO

PC112B Series

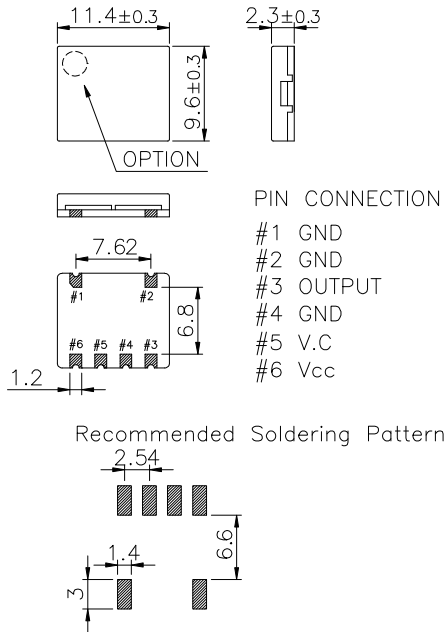
Clipped sinewave

6PAD SMD PACKAGE

* PART NUMBERING GUIDE



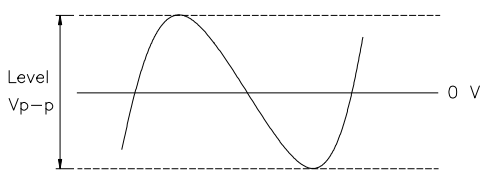
MECHANICAL DIMENSIONS



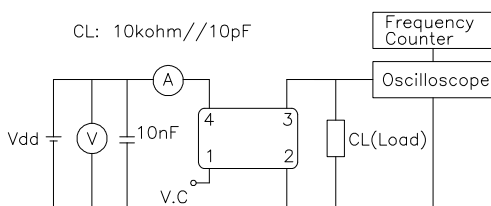
ELECTRICAL SPECIFICATION

Frequency range	10.000MHz to 40.000MHz Contact us if need high frequency
Frequency Stability vs. Temperature vs. Supply Voltage vs. Load vs. Aging	± 0.5 ppm to ± 5.0 ppm ± 0.2 ppm max / $V_{dd} \pm 5\%$ ± 0.2 ppm max / $15\text{pF} \pm 10\%$ ± 1.0 ppm max/ year
Temperature Range Operating Storage	See Table 2 -55°C to 125°C
Supply Voltage	$3.3\text{V} \pm 5\%$ $5.0\text{V} \pm 5\%$
Input Current Clipped sinewave	10.00MHz ~ 40.000MHz 2.0mA max ~ 4mA max
Output characteristics	Clipped sinewave Level 3.3V 0.8Vp-p min 5.0V 1.0Vp-p min Load $10\text{k}\Omega//10\text{pF}$
Phase Noise (typical) 20MHz offset	-80 dBc / Hz @ 10Hz -120 dBc / Hz @ 100Hz -135 dBc / Hz @ 1KHz -140 dBc / Hz @ 10KHz -145 dBc / Hz @ 100KHz
Frequency Adjustment	$\pm 3\text{ppm}$ min by internal trimmer (OPTION)
Voltage Control Characteristics	
Output Pulling Range ($\Delta F/\Delta V$)	$\pm 5.0\text{ppm}$ or $\pm 10\text{ppm}$ min $(\Delta F/\Delta V > \pm 20\text{ppm}$ is available, please contact us)
Control Voltage Range	$1.65\text{V} \pm 1.5\text{V}$ ($V_{dd} : 3.3\text{V}$), $2.5\text{V} \pm 2.0\text{V}$ ($V_{dd} : 5.0\text{V}$)

OUTPUT WAVEFORM



TEST CIRCUIT



ENVIROMENTAL & MECHANICAL SPECIFICATION

Shock	MIL-STD-883C, Method 2002, Condition B
Vibration	MIL-STD-883C, Method 2007, Condition A
Solderability	MIL-STD-883C, Method 2003
Seal integrity	MIL-STD-883C, Method 1014, Condition C & A2
Marking	MIL-STD-202F, Method 215

TABLE1

Symbol	Stability
05	$\pm 0.5\text{ppm}$
10	$\pm 1.0\text{ppm}$
15	$\pm 1.5\text{ppm}$
20	$\pm 2.0\text{ppm}$
25	$\pm 2.5\text{ppm}$
30	$\pm 3.0\text{ppm}$
35	$\pm 3.5\text{ppm}$
50	$\pm 5.0\text{ppm}$

TABLE2

Symbol	Temp.	Symbol	Temp.
0	0°C	A	50°C
1	-10°C	B	60°C
2	-20°C	C	70°C
3	-30°C	D	75°C
4	-40°C	E	80°C
		F	85°C